

FORM PTO 1449 (modified)  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  LIST OF REFERENCES CITED BY APPLICANT(S) (Use several sheets if necessary)				ATTY DOCKET NO. <b>NL020321US</b>		APPLICATION NO. <b>10/511,512</b>	
				APPLICANT <b>Jeroen Anton Johan Leijten</b>			
				FILING DATE <b>October 14, 2004</b>		GROUP <b>2183</b>	

  

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		<b>5,274,831</b>	<b>12/28/1993</b>	<b>Katsuta</b>			
		<b>5,452,401</b>	<b>9/19/1995</b>	<b>Lin</b>			
		<b>5,832,202</b>	<b>11/3/1998</b>	<b>Slavenburg, et al.</b>			
		<b>5,909,565</b>	<b>6/1/1999</b>	<b>Morikawa et al.</b>			
		<b>6,317,820 B1</b>	<b>11/13/2001</b>	<b>Shiell et al.</b>			
		<b>2001/049781 A1</b>	<b>12/6/2001</b>	<b>Miyake et al.</b>			

  

FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT
		<b>100 08 008 C1</b>	<b>8/23/2001</b>	<b>DE</b>			<b>w/ machine Engl. translation</b>
		<b>8-305568 A</b>	<b>11/22/1996</b>	<b>JP</b>			<b>w/ Engl. abst., see ctrpart US 5,909,565</b>
		<b>2001-243066 A</b>	<b>9/7/2001</b>	<b>JP</b>			<b>w/ Engl. abst., see ctrpart US 2001/049781</b>
		<b>00/26772 A1</b>	<b>5/11/2000</b>	<b>WO</b>			<b>English original</b>

  

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)	
	<b>Wu, "Power Analysis of Gated Pipeline Registers", ASIC/SOC Conf., IEEE, pgs. 281-85 (9/1999)</b>
	<b>Moon, "A 32-bit RISC Microprocessor With DSP Functionally: Rapid Prototyping", IEICE Trans. on Funds. of Electronics, Communications and Computer Sciences, vol. E84-A, no. 5, pgs. 1339-47 (5/2001)</b>

  

EXAMINER	DATE CONSIDERED
----------	-----------------

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.